

CLAIMS

What is claimed is:

1. A field-sequential liquid crystal display panel, comprising:
thin film transistors, each comprising a drain, a source, and a gate;
cell electrodes respectively coupled to the drains or sources of the thin film transistors;
scan electrode lines coupled to the gates of the thin film transistors;
data electrode lines coupled to the sources or drains of the thin film transistors; and
storage capacitors provided between each of the cell electrodes and a respective one of the scan electrode lines, to sustain voltages applied to the cell electrodes.
2. The field-sequential liquid crystal display panel of claim 1, wherein the storage capacitors are each provided between one of the cell electrodes and one of the scan electrode lines that is adjacent to a scan electrode line coupled to the respective one cell electrode through one of the thin film transistors.
3. The field-sequential liquid crystal display panel of claim 2, wherein the adjacent scan electrode line and the scan electrode line coupled to the respective one cell electrode are provided at opposite sides of the respective one cell electrode.
4. The field-sequential liquid crystal display panel of claim 1, further comprising a data driver to drive the data electrode lines.
5. The field-sequential liquid crystal display panel of claim 1, wherein the storage capacitors are each provided between one of the cell electrodes and a scan electrode line coupled to the respective one cell electrode through one of the thin film transistors.
6. The field-sequential liquid crystal display panel of claim 1, wherein a capacitance of the storage capacitors is approximately 0.07 pF to 0.2 pF.
7. The field-sequential liquid crystal display panel of claim 1, further comprising a scan driver to drive the scan electrode lines.

8. A field-sequential liquid crystal display panel, comprising:
cell electrodes;
scan electrode lines; and
storage capacitors to sustain voltage applied to the cell electrodes;
wherein the storage capacitors are provided between the cell electrodes and the scan electrode lines.
9. The field-sequential liquid crystal display panel of claim 8, further comprising thin film transistors respectively coupled to each of the cell electrodes, wherein the storage capacitors are each provided between one of the cell electrodes and one of the scan electrode lines that is adjacent to a scan electrode line coupled to the respective one cell electrode through one of the thin film transistors.
10. The field-sequential liquid crystal display panel of claim 8, further comprising thin film transistors respectively coupled to each of the cell electrodes, wherein the storage capacitors are each provided between one of the cell electrodes and a scan electrode line coupled to the respective one cell electrode through one of the thin film transistors.
11. The field-sequential liquid crystal display panel of claim 8, wherein a capacitance of the storage capacitors is approximately 0.07pF to 0.2pF.
12. The field-sequential liquid crystal display panel of claim 8, wherein the voltage is sustained in the storage capacitors between an ending point of scanning each of the respective scan electrode lines and a starting point of a lighting time which is applied to ones of the cell electrodes.
13. The field-sequential liquid crystal display panel of claim 8, further comprising a glass substrate, wherein the scan electrode lines are provided on the glass substrate.
14. The field-sequential liquid crystal display panel of claim 13, further comprising:
data electrode lines to drive the cell electrodes; and
an insulating layer provided on the data electrode lines;

wherein the cell electrodes are formed on the insulating layer.

15. The field-sequential liquid crystal display panel of claim 8, wherein the storage capacitors are formed by arranging the cell electrodes so that upper portions of the cell electrodes are disposed under the scan electrode lines.